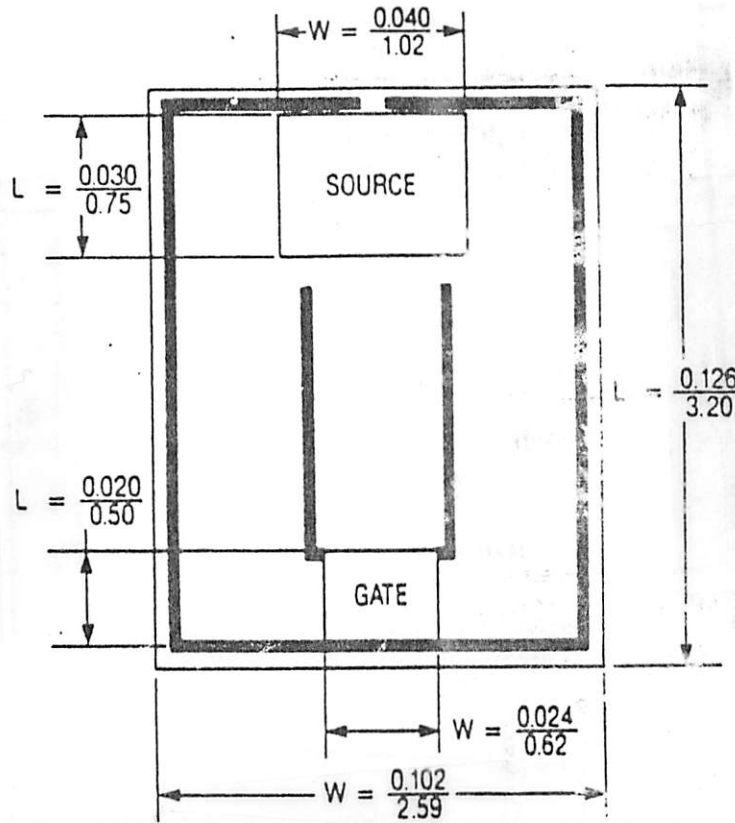




# Sierra Components, Inc.

924 Incline Way • Suite F • Incline Village, Nevada 89451  
 Phone: 775.831.1241 Fax: 775.831.3021

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Topside Metal: Al**  
**Backside: Cr Ni Ag**  
**Backside Potential:**  
**Mask Ref:**  
**Bond Pads : .004"**

**APPROVED BY: CB**  
**MFG: IR**

**DIE SIZE : .126" x .102"**  
**THICKNESS: .016"**

**DATE: 2/7/01**  
**P/N: IRFC024**

DG 10.1.2  
 Rev A 3-4-99